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TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING REJECTION OVER A "PRIOR" PATENT	Docket Number (Optional) 8021-225JHM/SS18860US
In re Application of: Kyo-Min Sohn, et al.	
Application No.: 10/814,968	
Filed: March 31, 2004	
FOI: INTEGRATED CIRCUIT HAVING A MEMORY CELL ARRAY CAPABLE OF SIMULTANEOUSLY OPERATION AND A DATA WRITE OPERATION	PERFORMING A DATA READ
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2. The undersigned is an attorney or agent of record. Reg. No. 27,213	
Jan Al Vaiola	March 19, 2008
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